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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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12/16/1998

CHINNA PRUDVI

2207/5915

8642

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KENYON & KENYON LLP
1500 K STREET N.W.
SUITE 700
WASHINGTON, DC 20005

EXAMINER

THAI, TUAN V

ART UNIT

PAPER NUMBER

2185

MAIL DATE

DELIVERY MODE

11/12/2008

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

DETAILED ACTION

1. This office action is in response to Applicant's communication filed August 14, 2008. This amendment has been entered and carefully considered. Claims 1-4, 6-7, 11-16 and 30-37 are presented for examination. Claims 5, 8-10 and 17-29 have been canceled. Claims 30-37 are newly added. Claims 30-33 and 37 are now canceled.

2. Newly submitted claims 30-33 and 37 directed to an invention that is independent or distinct from the invention originally claimed for the following reasons: the invention of groups I (claims 1-4, 6-7, 11-16 and 34-36) and group II (claims 30-33 and 37) are related as combination/subcombinations disclosed as usable together in a single combination. In the instant case, the invention of group I has separate utility such as implementation of processing agent having *internal cache with multiple cache lines for storing multiple data lines length of data* could be used in numerous different data storage systems and is not limited for use with processing agent with transaction queue having multiple queue entries, wherein each entry has address field and multiple status fields for storing data interlinked with transactions related with first and second address of the address field of group II. Similarly, the

Art Unit: 2185

invention of group II can be used as system for providing access to addressable entities using sequential/non-sequential address structure and is not restricted for use with a processing agent with an internal cache having multiple cache lines wherein each cache line for storing address and multiple data line lengths of data and a transaction queue for transferring data in an external transaction of group I. See M.P.E.P. § 806.05.

Since applicant has received an action on the merits for the originally presented invention, this invention has been constructively elected by original presentation for prosecution on the merits. Accordingly, claim 30-33 and 37 are withdrawn from consideration as being directed to a non-elected invention. See 37 CFR 1.142(b) and MPEP § 821.03.

Allowable Subject Matter

3. Claims 1-4, 6-7, 11-16 and 34-36 are allowed.

4. The following is an Examiner's Statement of Reasons for Allowance:

The prior arts of record do not teach nor suggest, *either alone or in combination*, **all** the limitations of the amended claims (claims 1 and 11) of the current invention. The prior

Art Unit: 2185

arts of record do not teach a processing agent with an internal cache having multiple cache lines wherein each cache line for storing address and multiple data line lengths of data *and* a transaction queue with multiple queue entries having address and status portions, wherein status portions provided for external transactions related to data portions of respective cache lines (claim 1); nor does the prior arts discloses the transaction queue system comprises a plurality of queue entries wherein each queue entry providing a sequence of external transactions, each external transactions of the sequence related to a single data line and the sequence of external transactions related to a single cache line (claim 11). In light of the foregoing, claims 1 and 11 of the present application are found to be patentable over the prior arts.

Claims 2-4, 6-7, 34-36 and 12-16 further limit the allowable independent claims 1 and 11 respectively. These claims are therefore allowable for the same reason as set forth above.

5. This application is in condition for allowance except for the following formal matters:

The objections to the claims as noted above.

Prosecution on the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

A shortened statutory period for reply to this action is set to expire **TWO MONTHS** from the mailing date of this letter.

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan V. Thai whose telephone number is (571)-272-4187. The examiner can normally be reached on from 6:30 A.M. to 4:00 P.M..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sanjiv Shah can be reached on (571)-272-4098. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact

Application/Control Number: 09/212,291

Page 6

Art Unit: 2185

the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TVT/November 07, 2008

/Tuan V. Thai/

Primary Examiner, Art Unit 2185